



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C		
60V	7.5Ω @ V _{GS} = 5V	210mA		

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

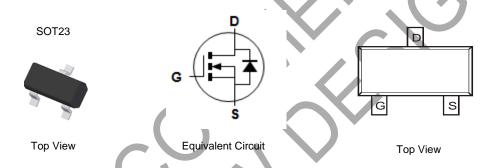
- Motor control
- **Power Management Functions**

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)



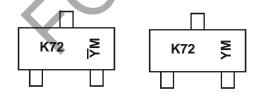
Ordering Information (Note 4)

Part Number	Case	Packaging
2N7002-7-02	SQT23	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Chengdu A/T Site

K72 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	N	Р	R	S	T	U	V	W	Χ	Υ	Z	Α	В	C	D	Е
Month	Jan	F	eb	Mar	Apr	М	ay	Jun	Jul	Α	ug	Sep	Oct	No	ov	Dec
Code	1		2	3	4		5	6	7		8	9	0	1	7	D

Shanghai A/T Site



Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	60	V
Drain-Gate Voltage R _{GS} ≤ 1.0MΩ			V_{DGR}	60	V
Gate-Source Voltage		Continuous Pulsed	V_{GSS}	±20 ±40	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25$ °C $T_A = +85$ °C $T_A = +100$ °C	I _D	170 120 105	mA
Continuous Drain Current (Note 6)	Steady State	$T_A = +25$ °C $T_A = +85$ °C $T_A = +100$ °C	I _D	210 150 135	mA
Maximum Body Diode Forward Current (Note 6)	•	Continuous	I _S	0.5	А

Thermal Characteristics

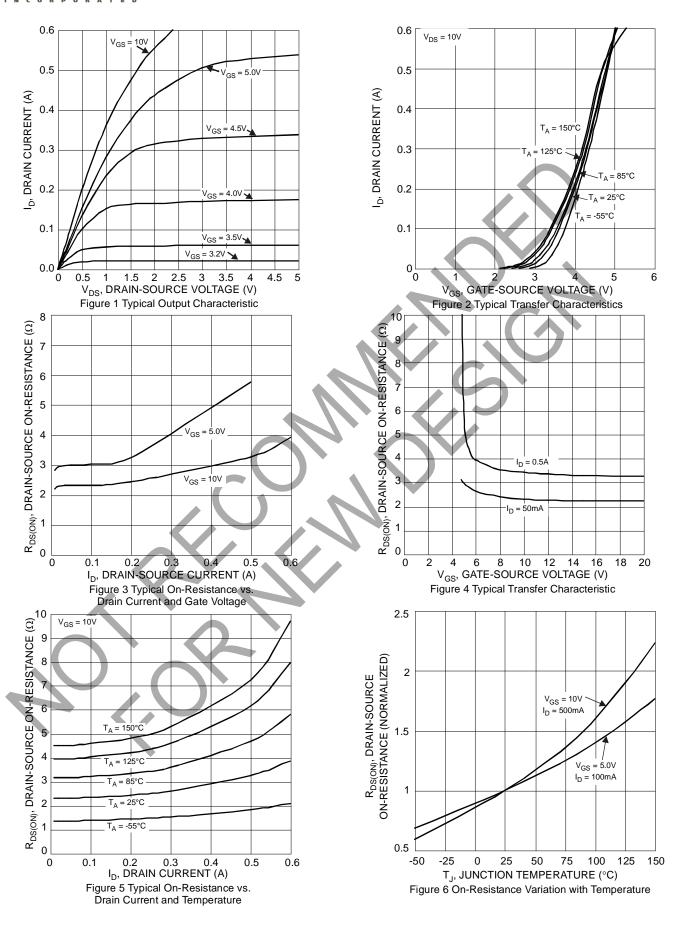
Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 5)	C	380	mW
Total Fower Dissipation	(Note 6)	P _D	520	IIIVV
Thermal Resistance, Junction to Ambient	(Note 5)	Reja	335	
Thermal Resistance, Junction to Ambient	(Note 6)	ReJA	243	°C/W
Thermal Resistance, Junction to Case	(Note 6)	$R_{ heta JC}$	89	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

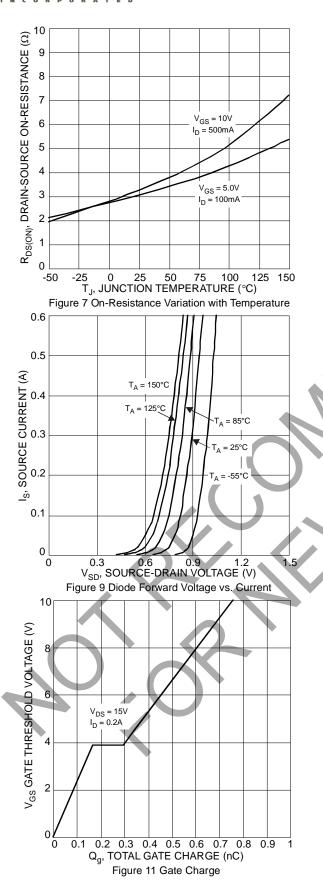
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current $@T_C = +25^\circ$ $@T_C = +125^\circ$	DCC	_	_	1.0 500	μA	V _{DS} = 60V, V _{GS} = 0V	
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage $@T_J = +2$ $@T_J = +0$ °C~ +100°C (No		2.1 1.8	_	2.55 2.65	V	$V_{DS} = V_{GS}, I_D = 250\mu A$ $V_{DS} = V_{GS}, I_D = 250\mu A$	
Static Drain-Source On-Resistance $@T_J = +25^\circ$ $@T_J = +125^\circ$	KDC (ON)	_	_	7.5 13.5	Ω	$V_{GS} = 5.0V, I_D = 0.05A$ $V_{GS} = 10V, I_D = 0.5A$	
On-State Drain Current	I _{D(ON)}	0.5	_	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$	
Forward Transconductance	g _{FS}	80	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$	
Diode Forward Voltage	V _{SD}	_	0.78	1.5	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)			•	•	•	•	
Input Capacitance	C _{iss}	_	_	50	pF	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C_{rss}			5.0	pF	1 = 1.0WI12	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.34	_			
Gate-Source Charge	Q_{gs}	_	0.17	_	рC	$V_{DS} = 15V, I_D = 0.2A$	
Gate-Drain Charge	Q_{gd}	_	0.13	_			
SWITCHING CHARACTERISTICS (Note 8)							
Turn-On Delay Time	t _{D(on)}		3.2	_			
Turn-On Rise Time	t _r	_	2.4	_	ns	$V_{DD} = 30V, I_D = 0.2A,$	
Turn-Off Delay Time	t _{D(off)}		7.5		115	$R_L = 150\Omega, V_{GEN} = 10V,$	
Turn-Off Fall Time	t _f	_	3.8	_		$R_{GEN} = 25\Omega$	

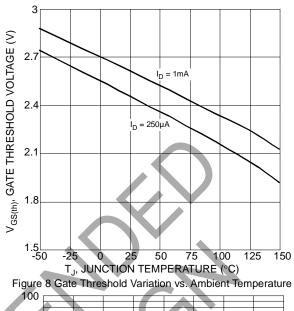
- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

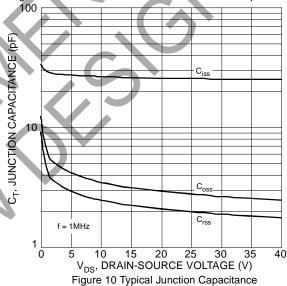








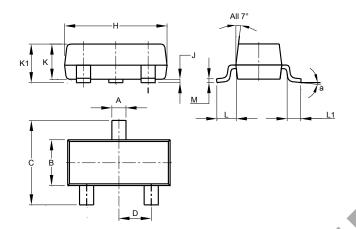






Package Outline Dimensions

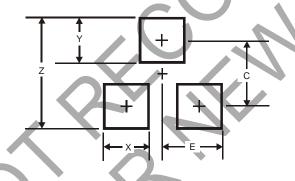
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а		8°						
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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